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PATENT 3/23/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mark McQueen

Serial No.: 09/146,851

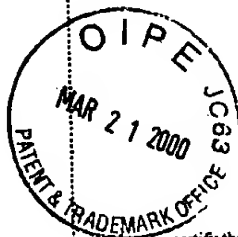
Filed: September 3, 1998

For: ESD/EOS PROTECTION
STRUCTURE FOR INTEGRATED
CIRCUIT DEVICES AND METHODS OF
FABRICATING THE SAME

Examiner: D. Le

Group Art Unit: 2818

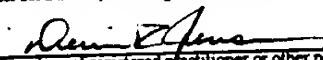
Attorney Docket No.: 3522US (97-1080)



CERTIFICATE OF MAILING

Thereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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1.8(a)(1)(ii)

Devin R. Jensen
Typed/printed name of person whose signature is
contained above

RESPONSE TO RESTRICTION REQUIREMENT

Box Non-Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicant herein acknowledges the restriction requirement in the above-referenced application. Applicant hereby elects the claims of Group I, claims 1, 3-10, 19, and 21-28 without traverse.

Applicant notes that the Official Action includes claim 19 in both Group I and Group II. As claim 19 is not a method claim, Applicant assumes that the Examiner intended claim 19 to fall within Group I. If this assumption is in error, Applicant requests that the Examiner notify Applicant of such at the Examiner's earliest convenience.

Serial N . 09/146,851

An early Office Action on the merits is respectfully solicited.

Respectfully submitted,



Devin R. Jensen

Registration No. 44,805

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DRJ/ps:kf

Date: March 17, 2000

N:\2269\3522\Restriction Requirement